



NATIONAL INSTITUTE OF TECHNOLOGY
TIRUCHIRAPPALLI- 620 015

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APPLICATION FOR THE TEMPORARY POST OF PROJECT DESIGN ENGINEER
IN ALGORITHMIC APPROACH TO ACHIEVE MAXIMUM FUNCTIONAL COVERAGE USING
CONSTRAINED RANDOM VERIFICATION PLATFORM
-DEMONSTRATION OF PROOF OF CONCEPT

Registration Number : ECE/2021/_____

1.	Name in Full (Capital Letters) (as in SSLC Certificate)					
2.	Date of Birth	Date	Month	Year		
		<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
3.	Gender	Male/Female				
4.	Category	Gen/OBC/SC/ST				
5.	Marital status	Single/Married				
6.	Address to which Communications should be sent including Email id: Mobile number:					
7.	Permanent Address					
8. Education details (from SSLC onwards)						
	Degree	Discipline	College of study	Class/Division	% Marks	Year of Passing

9. Professional Experience in chronological order up to the present post (Add separate sheet if needed)				
Organization	Designation	From	To	Experience (in years)
Total experience (in years)				
10.	Publication details (Add separate sheet if needed)			
	Conference publications			
	Journal publications			
11.	Details of FPGA/VLSI CAD tools experience (Add separate sheet if needed)			
12.	List training programmes attended (Add separate sheet if needed)			
13.	Details of GATE/NET score (if applicable): Score: _____ Year of passing: _____ Valid up to _____			
14.	Any other information you wish to provide: (Add separate sheet if needed)			
15.	I hereby declare that the information given above is correct to the best of my knowledge and belief. I fully understand that if it is found later that any information given in the application is incorrect/ false or if I do not satisfy the eligibility criteria, my candidature/ appointment is liable to be cancelled / terminated. I understand that the post is purely temporary in nature Place : _____ Date : _____ Signature of the Applicant			